

Hesam Sadeghi

Personal Information

Address Integrated Systems Design Laboratory, EE. Department, Sharif University of Technology

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Education

Sep. 2012 to Present **Sharif University of Technology**, Tehran, Iran
Master of Science in Electrical Engineering
Current GPA: 17.38
Thesis: Modeling and improvement of phase noise in Ring oscillator
Supervisor: Dr. Sharif-Bakhtiar

Sep. 2008 to Sep. 2012 **Isfahan University of Technology**, Isfahan, Iran
Bachelor of Science in Electrical Engineering
GPA: 18.91
Supervisor: Dr. Ghafarinia

Research Interests

- RF Integrated Circuit Design
 - Low Power and Low Voltage Circuit Design
 - Biomedical Circuit Design
 - Analog/Mixed-signal Circuit Design
 - Nanoelectronic Devices
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Honors and Awards

- Honorary Admission to Master's Program in Microelectronics at Sharif University of Technology without Taking the Nationwide University Entrance Exam for M.Sc. Degree, Winter 2012.
- Ranked 3rd among electrical engineering undergraduate students of ECE department, Isfahan University of Technology.
- Member of Iran's National Elites Foundation

Research Experiences

<i>Fall 2013</i>	Review of “ <i>Tunneling FET</i> ”, Dr. Etemadi
<i>Fall 2013</i>	Review of “ <i>SOI technology and it’s High temperature applications</i> ”, Dr. Movahedian
<i>Spring 2013</i>	Design of “ <i>4G/LTE Transceiver</i> ” Using 0.18 micron Technology, Dr. Fotowat-Ahmady
<i>Spring 2013</i>	Design of “ <i>switched capacitor filters</i> ” Using 0.18 micron Technology, Dr. Sharif-Bakhtiar
<i>Fall 2012 to Present</i>	Modeling and improvement of “ <i>phase noise in Ring oscillator</i> ”, Dr. Sharif-Bakhtiar
<i>Fall 2012</i>	Study “ <i>Effect of limiter and buffer on phase noise</i> ”, Dr. Sharif-Bakhtiar
<i>Fall 2012</i>	Design of “ <i>Low noise oscillator with Active Inductor</i> ”, Dr. Sharif-Bakhtiar
<i>Fall 2012</i>	Design of “ <i>low power 64-bit adder</i> ” Using 0.18 micron Technology, Dr. Sharif Khani
<i>Fall 2011</i>	Design of “ <i>An analog integrated signal processing circuit for on-chip diffusion-based gas analysis</i> ”, Dr. Ghafarinia
<i>Fall 2011</i>	Review of “ <i>gas sensors</i> ”, Dr. Ghafarinia

Related Courses

Passed

- “*CMOS Circuit Design I*”, Dr. Sharif-Bakhtiar
- “*CMOS Circuit Design II*”, Dr. Sharif-Bakhtiar
- “*RFIC*”, Dr. Fotowat-Ahmady
- “*Digital Electronics*”, Dr. Sharif Khani
- “*Data Conversion Circuit Design*”, Dr. Haj-Sadeghi
- “*Semiconductor Technology*”, Dr. Faez
- “*Advance Physics of Semiconductor Devices II*”, Dr. Etemadi
- “*Harsh Electronics*”, Dr. Movahedian
- “*Analog Electronics*”, Dr. Dehghani
- “*Vlsi Design*”, Dr. Ghafarinia
- “*Communication Circuits*”, Dr. Sadri

Publication

- Sadeghi, Hesam, and Vahid Ghafarinia. "*An analog integrated signal processing circuit for on-chip diffusion-based gas analysis*," Measurement Science and Technology 24.7 (2013): 075802.

Computer Skills

Programming Languages MATLAB, C/C++, Verilog

CADs & Applications Agilent ADS, Cadence Virtuoso, ORCAD, HSpice, Proteus

Extracurricular Activities

- Football, Cycling and Chess